

PRM1R6N04HN5

PFC Device Corporation

40V Single N-Channel MOSFET

Major ratings and characteristics

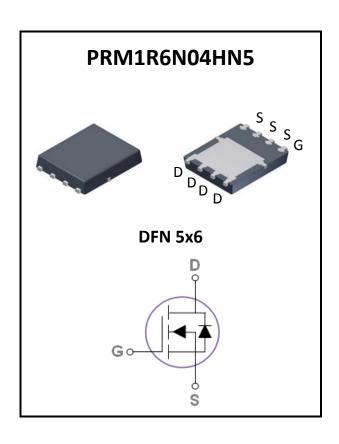
Characteristics	Values	Units
V_{DS}	40	V
$I_D^5(T_C=25^{\circ}C)$	202	Α
Max. R _{DS(ON)} @V _{GS} =10V	1.6	mΩ
T _J Operating Junction Temperature	-55 to +150	°C

General Description

The N-Channel enhancement mode power field effect transistor is using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. The device is well suited for high efficiency fast switching applications.

Typical Applications

- Charger Adapter
- Power Tools
- LED Lighting



Features

- Max. $R_{DS(ON)}=1.6m\Omega@V_{GS}=10V$
- Improved dv/dt capability
- Fast switching
- 100% E_{AS} Guaranteed
- Green Device Available

1. Characteristics

Maximum Ratings Characteristics

($T_A = 25$ °C unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	±20	V
I _D ⁵	Drain Current – Continuous (T _C =25°C)	202	Α
I _D	Drain Current – Continuous (T _C =100°C)	127	А
I_D^6	Drain Current – Continuous (TC=25°C)	60	Α
I _{DM}	Drain Current – Pulsed ¹	240	А
E _{AS}	Single Pulse Avalanche Energy ²	80	mJ
I _{AS}	Single Pulse Avalanche Current ²	40	Α
В	Power Dissipation (T _C =25°C)	83	W
P _D	Power Dissipation – Derate above 25°C	0.7	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient		60	°C/W
$R_{ heta JC}$	Thermal Resistance Junction to Case		1.5	°C/W



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Electrical Characteristics

($T_J = 25$ °C unless otherwise specified)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	40			V
		V _{DS} =40V, V _{GS} =0V, T _J =25°C			1	uA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =32V, V _{GS} =0V, T _J =125°C			10	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA

On Characteristics

	R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =20A		1.3	1.6	mΩ
	$V_{GS(th)}$	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	2.0	2.8	4.0	V
ĺ	g_{fs}	Forward Transconductance	V _{DS} =5V, I _D =20A		88		S

Dynamic and switching Characteristics

Q_{q}	Total Gate Charge ^{3, 4}	V _{DS} =20V, V _{GS} =10V, I _D =20A	 94	
Q_{gs}	Gate-Source Charge ^{3, 4}		 13.7	 nC
Q_{qd}	Gate-Drain Charge ^{3, 4}		 22.4	
$T_{d(on)}$	Turn-On Delay Time ^{3, 4}		 22	
T _r	Turn-On Rise Time ^{3, 4}	V_{DD} =20V, V_{GS} =10V, R_{G} =3 Ω	 108	 ne
$T_{d(off)}$	Turn-Off Delay Time ^{3, 4}		 72	 ns
T_f	Turn-Off Fall Time ^{3, 4}		 37	
C_{iss}	Input Capacitance		 5243	
C_{oss}	Output Capacitance	V _{DS} =20V, V _{GS} =0V, f=1MHz	 1602	 pF
C_{rss}	Reverse Transfer Capacitance		 68	
R_{q}	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz	 0.7	 Ω

Drain-Source Diode Characteristics

V_{SD}	Source to Drain Diode Voltage	$V_{GS}=0V$, $I_{S}=1A$	ł	1	1	V
t _{rr}	Reverse Recovery Time	1 204 di/dt 1004/us		53		ns
Q_{rr}	Reverse Recovery Charge	I _S =20A, di/dt=100A/us		67		nC

Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. VDD=50V, VGS=10V, L=0.1mH, RG=25 Ω , Starting TJ=25 $^{\circ}$ C
- 3. The data tested by pulsed, pulse width ≤300us, duty cycle ≤2%.
- 4. Essentially independent of operating temperature.
- 5. Silicon limited.
- 6. Package limited.



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2. Characteristics Curves

Ratings and Characteristics Curves

(T_A = 25° unless otherwise specified)

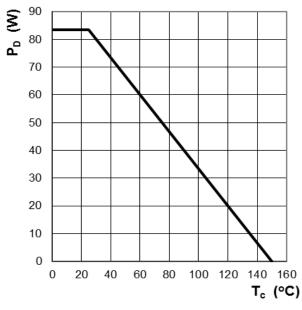


Figure 1: Power Dissipation

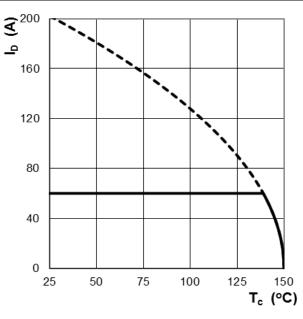


Figure 2: Continuous Drain Current vs. T_C

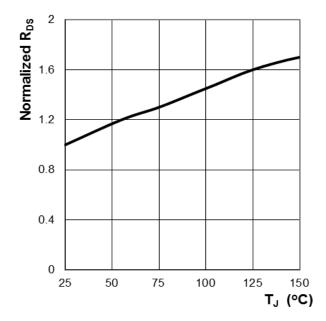


Figure 3: Normalized R_{DS(ON)} vs. T_J

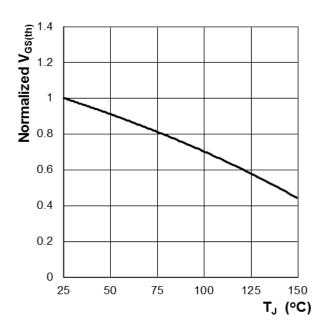


Figure 4: Normalized Vth vs. T_J



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Ratings and Characteristics Curves

($T_A = 25^{\circ}C$ unless otherwise specified)

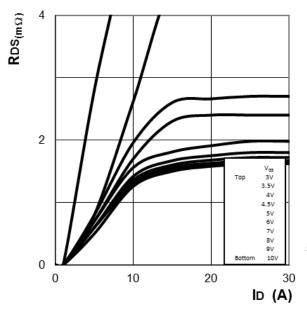


Figure 5: RDS(ON) vs. Drain Current and Gate Voltage

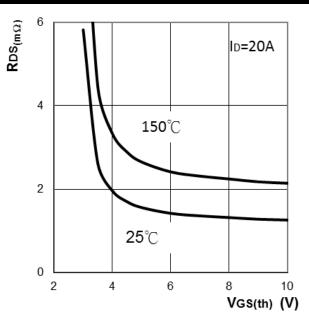


Figure 6: RDS(ON) vs. Gate Voltage

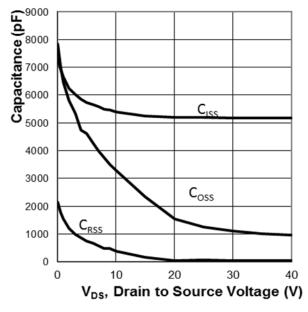


Figure 7: Typ. Capacitance Characteristics

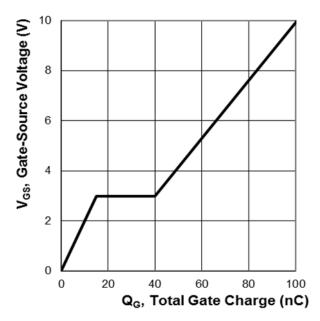


Figure 8: Typ. Gate Charge Characteristics



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Ratings and Characteristics Curves

(T_A = 25°C unless otherwise specified)

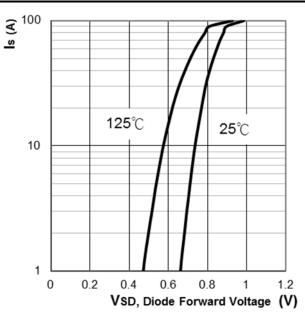


Figure 9: Body Diode Characters

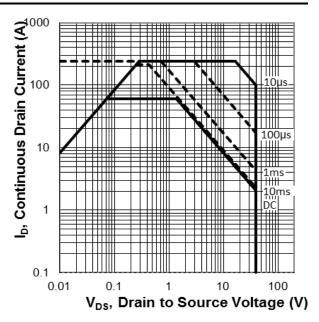


Figure 10: Maximum Safe Operation Area

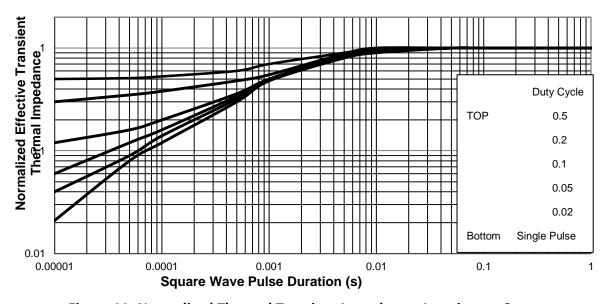


Figure 11: Normalized Thermal Transient Impedance, Junction-to-Case



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3. Marking information

Top Marking Rule

PFC PRM
1R6N04HN5
YYWW ABSH

PRM1R6N04HN5 = Product Type Marking Code

YYWW = Date Code

YY = Last two digits of year

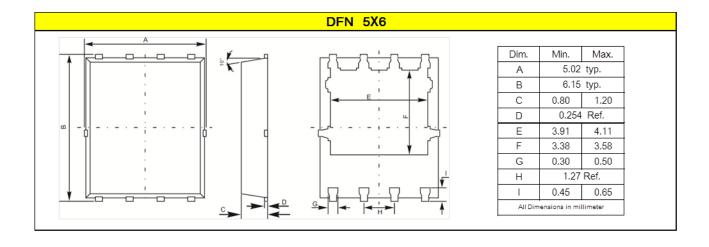
WW = Week code

ABS = Assembly code

H = Halogen Free (N/A = common molding compound)

4. Package information

Package Outline Dimensions millimeters





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5. Ordering information

Part Number	Package Delivery mod	
PRM1R6N04HN5	DFN 5X6	5000 pcs / 13" diameter reel

Mechanical

Molder Plastic: UL Flammability Classification Rating 94V-0

■ Device Weight: 0.003 ounces (0.093grams) – DFN 5x6

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