

PRM1R8N04N5

PFC Device Corporation

40V Single N-Channel MOSFET

Major ratings and characteristics

Characteristics	Values	Units
V_{DS}	40	٧
$I_D^4 (T_C=25^{\circ}C)$	190	Α
Max. R _{DS(ON)} @V _{GS} =10V	1.8	mΩ
Max. R _{DS(ON)} @V _{GS} =4.5V	2.4	mΩ
T _J Operating Junction Temperature	-55 to +150	°င

General Description

The N-Channel enhancement mode power field effect transistor is using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. The device is well suited for high efficiency fast switching applications.

PRM1R8N04N5 DFN 5x6

Typical Applications

- Charger Adapter
- Power Tools
- LED Lighting

Features

- Max. R_{DS(ON)}=1.8mΩ@V_{GS}=10V
- Improved dv/dt capability
- Fast switching
- 100% E_{AS} Guaranteed
- Green Device Available

1. Characteristics

Maximum Ratings Characteristics

($T_A = 25$ °C unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	±20	V
l _D ⁴	Drain Current – Continuous (T _C =25°C)	190	Α
ID	Drain Current – Continuous (T _C =100°C)	120	Α
I_D^5	Drain Current – Continuous (T _C =25°C)	100	Α
I _{DM}	Drain Current – Pulsed ¹	400	Α
E _{AS}	Single Pulse Avalanche Energy ²	52	mJ
I _{AS}	Single Pulse Avalanche Current ²	32	Α
	Power Dissipation (T _C =25°C)	89	W
P _D	Power Dissipation – Derate above 25°C	0.7	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
$R_{ heta JA}$	Thermal Resistance Junction to ambient		40	°C/W
$R_{ heta JC}$	Thermal Resistance Junction to Case		1.4	°C/W



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Electrical Characteristics

($T_J = 25$ °C unless otherwise specified)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	40			V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =32V, V _{GS} =0V, T _J =25°C			1	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA

On Characteristics

D	R _{DS(ON)} Static Drain-Source On-Resistance	V _{GS} =10V, I _D =20A		1.4	1.8	mΩ
K _{DS(ON)}	Static Dialii-Source Off-Resistance	V _{GS} =4.5V, I _D =10A		2.0	2.4	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_{D}=250uA$	1.0	1.7	2.5	V
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =10A		60		S

Dynamic and switching Characteristics

	and officering officerations					
Q_{g}	Total Gate Charge			60.0		
Q_gs	Gate-Source Charge	V _{DS} =20V, V _{GS} =10V, I _D =20A		10.3		nC
Q_gd	Gate-Drain Charge			12.3		
$T_{d(on)}$	Turn-On Delay Time			16		
T _r	Turn-On Rise Time	V_{DD} =20V, V_{GS} =10V, R_{G} =3 Ω	-	55	1	no
$T_{d(off)}$	Turn-Off Delay Time			90		ns
T_f	Turn-Off Fall Time		-	20	-	
C_{iss}	Input Capacitance		-	3276	1	
C_{oss}	Output Capacitance	V _{DS} =20V, V _{GS} =0V, f=1MHz	-	1031	-	pF
C_{rss}	Reverse Transfer Capacitance		-	75	-	
R_{g}	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz		0.6		Ω

Drain-Source Diode Characteristics

V_{SD}^{3}	Source to Drain Diode Voltage	$V_{GS}=0V$, $I_{S}=1A$	 	1.2	V
t _{rr}	Reverse Recovery Time	1 201 di/dt 1001/up	 33	-	ns
Q _{rr}	Reverse Recovery Charge	I _S =20A, di/dt=100A/us	 30		nC

Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. VDD=50V, VGS=10V, L=0.1mH, RG=25 Ω , Starting TJ=25 $^{\circ}$ C
- 3. The data tested by pulsed, pulse width \leq 300us, duty cycle \leq 2%.
- 4. Silicon limited.
- 5. Package limited.



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2. Characteristics Curves

Ratings and Characteristics Curves

(T_A = 25°C unless otherwise specified)

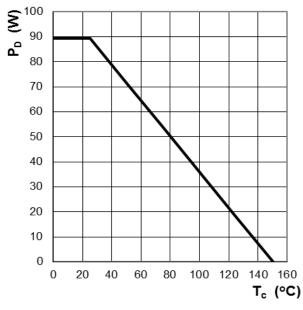


Figure 1: Power Dissipation

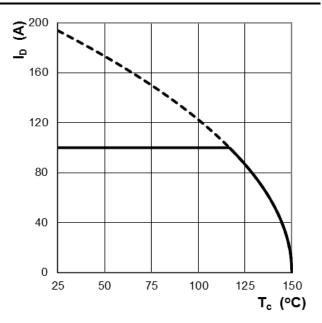


Figure 2: Continuous Drain Current vs. T_C

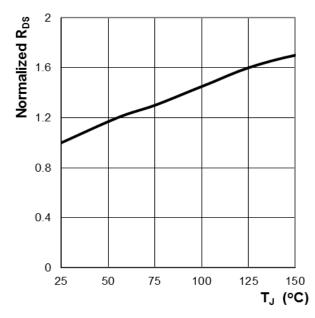


Figure 3: Normalized R_{DS(ON)} vs. T_J

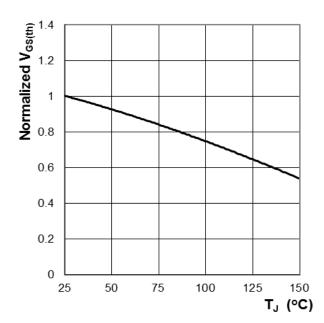


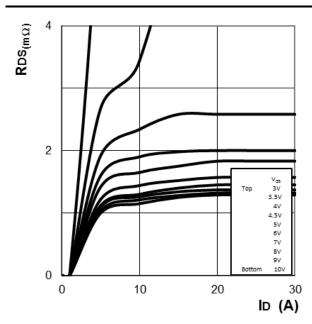
Figure 4: Normalized V_{GS(th)} vs. T_J



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Ratings and Characteristics Curves

($T_A = 25^{\circ}$ C unless otherwise specified)



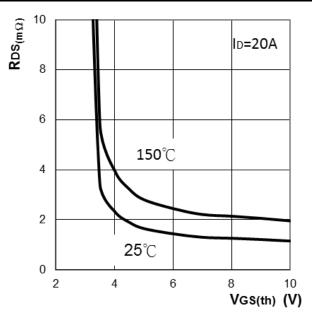


Figure 5: RDS(ON) vs. Drain Current and Gate Voltage

Figure 6: RDS(ON) vs. Gate Voltage

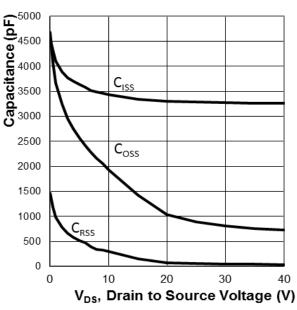


Figure 7: Typ. Capacitance Characteristics

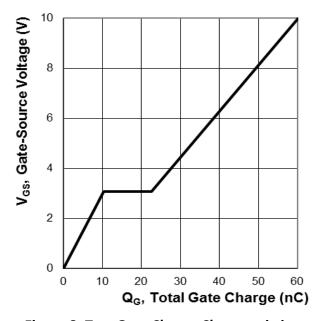


Figure 8: Typ. Gate Charge Characteristics



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Ratings and Characteristics Curves

(T_A = 25° unless otherwise specified)

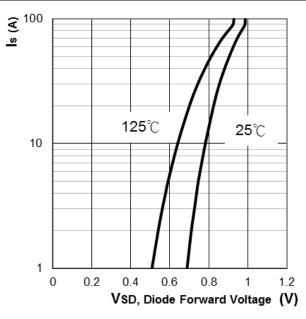


Figure 9: Body Diode Characters

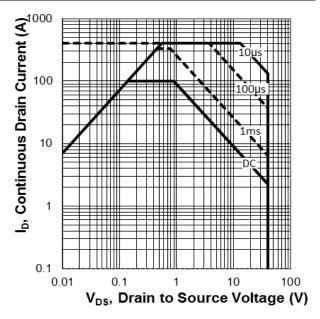


Figure 10: Maximum Safe Operation Area

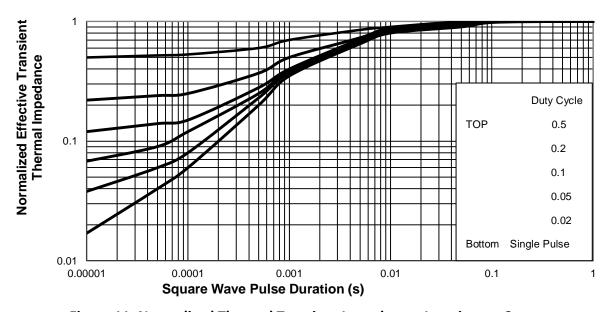


Figure 11: Normalized Thermal Transient Impedance, Junction-to-Case



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3. Marking information

Top Marking Rule

PFC PRM
1R8N04N5
YYWW ABSH

PRM1R8N05N5 = Product Type Marking Code

YYWW = Date Code

YY = Last two digits of year

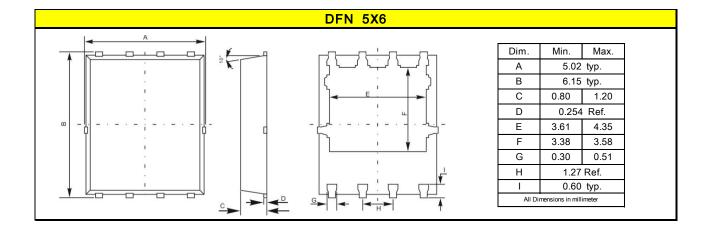
WW = Week code

ABS = Assembly code

H = Halogen Free (N/A = common molding compound)

4. Package information

Package Outline Dimensions millimeters





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5. Ordering information

Part Number	Package	Delivery mode
PRM1R8N04N5	DFN 5X6	3000 pcs / 13" diameter reel

Mechanical

Molder Plastic : UL Flammability Classification Rating 94V-0
 Device Weight : 0.003 ounces (0.093grams) – DFN 5X6

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