

PRM4R7N10N5

PFC Device Corporation

100V Single N-Channel MOSFET

Major ratings and characteristics

Characteristics	Values	Units
V_{DS}	100	V
$I_{D}^{5} (T_{C}=25^{\circ}C)$	127	Α
Max. R _{DS(ON)} @V _{GS} =10V	4.7	mΩ
Max. R _{DS(ON)} @V _{GS} =4.5V	5.7	mΩ
T _J Operating Junction Temperature	-55 to +150	°C

General Description

The N-Channel enhancement mode power field effect transistor is using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. The device is well suited for high efficiency fast switching applications.

PRM4R7N10N5 DFN 5x6

Typical Applications

- Charger Adapter
- Power Tools
- LED Lighting

Features

- Max. R_{DS(ON)}=4.7mΩ@V_{GS}=10V
- Improved dv/dt capability
- Fast switching
- 100% E_{AS} Guaranteed
- Green Device Available

1. Characteristics

Maximum Ratings Characteristics

($T_A = 25$ °C unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	±20	V
I _D ⁵	Drain Current – Continuous (T _C =25°C)	127	Α
ID	Drain Current – Continuous (T _C =100°C)	81	Α
I_D^6	Drain Current – Continuous (TC=25°C)	60	Α
I_{DM}	Drain Current – Pulsed ¹	240	Α
E _{AS}	Single Pulse Avalanche Energy ²	68	mJ
I _{AS}	Single Pulse Avalanche Current ²	37	Α
P _D	Power Dissipation (T _C =25°C)	139	W
FD	Power Dissipation – Derate above 25°C	1.1	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient		62	°C/W
$R_{ heta JC}$	Thermal Resistance Junction to Case		0.9	°C/W



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Electrical Characteristics

($T_J = 25$ °C unless otherwise specified)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	100			V
	Davis On and had a const	V _{DS} =100V, V _{GS} =0V, T _J =25°C			1	uA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =80V, V _{GS} =0V, T _J =125°C			10	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA

On Characteristics

P Statio Drain Source On Boy	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =20A		3.7	4.7	mΩ	
		V _{GS} =4.5V, I _D =10A		4.7	5.7	mΩ	
	$V_{GS(th)}$	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.0	1.6	2.5	V
	g_{fs}	Forward Transconductance	V_{DS} =5V, I_{D} =20A		59		S

Dynamic and switching Characteristics

Q_{g}	Total Gate Charge ^{3,4}	V _{DS} =80V, V _{GS} =10V, I _D =10A	 68		
Q_{qs}	Gate-Source Charge ^{3, 4}		 10		nC
Q_{gd}	Gate-Drain Charge ^{3, 4}		 16		
$T_{d(on)}$	Turn-On Delay Time ^{3,4}		 15		
T _r	Turn-On Rise Time ^{3, 4}	V_{DD} =80V, V_{GS} =10V, R_{G} =6 Ω	 23		ns
$T_{d(off)}$	Turn-Off Delay Time ^{3, 4}		 72		115
T_f	Turn-Off Fall Time ^{3, 4}		 129	-	
C _{iss}	Input Capacitance		 3653		
C _{oss}	Output Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz	 1898		pF
C_{rss}	Reverse Transfer Capacitance		 159		
R_{g}	Gate resistance	V_{GS} =0V, V_{DS} =0V, f=1MHz	 0.8		Ω

Drain-Source Diode Characteristics

V_{SD}	Source to Drain Diode Voltage	$V_{GS}=0V$, $I_{S}=1A$	 	1	V
t _{rr}	Reverse Recovery Time	1 20A di/dt 100A/ug	 66		ns
Q_{rr}	Reverse Recovery Charge	I _S =20A, di/dt=100A/us	 94		nC

Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. VDD=50V, VGS=10V, L=0.1mH, RG=25 Ω , Starting TJ=25 $^{\circ}$ C
- 3. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 4. Essentially independent of operating temperature.
- 5. Silicon limited.
- 6. Package limited.



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2. Characteristics Curves

Ratings and Characteristics Curves

(T_A = 25° unless otherwise specified)

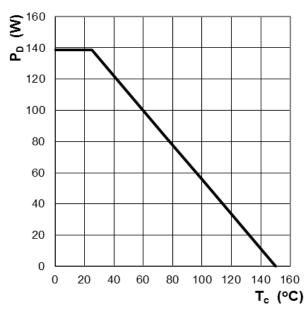


Figure 1: Power Dissipation

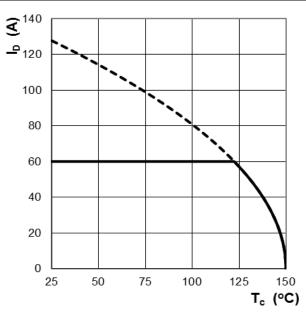


Figure 2: Continuous Drain Current vs. T_C

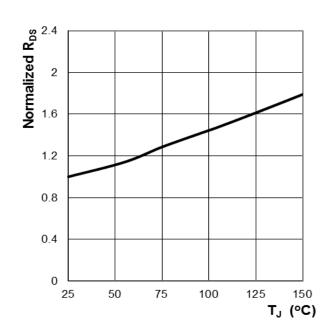


Figure 3: Normalized R_{DS(ON)} vs. T_J

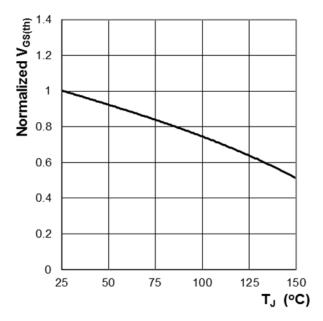


Figure 4: Normalized Vth vs. T_J



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Ratings and Characteristics Curves

($T_A = 25^{\circ}C$ unless otherwise specified)

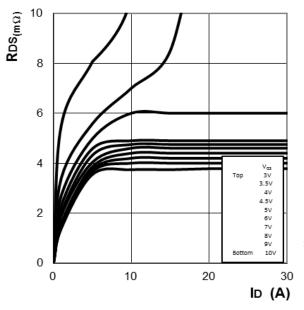


Figure 5: RDS(ON) vs. Drain Current and Gate Voltage

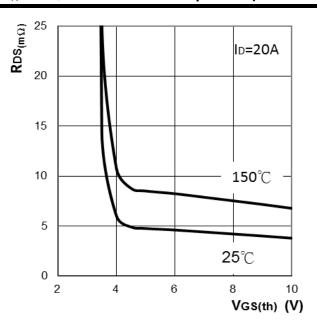


Figure 6: RDS(ON) vs. Gate Voltage

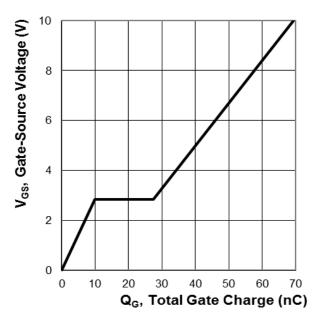


Figure 7: Typ. Gate Charge Characteristics

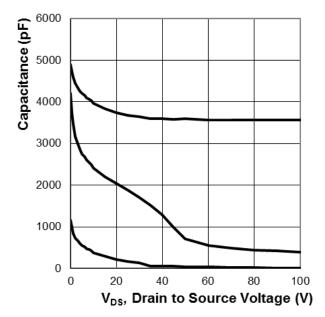


Figure 8: Typ. Capacitance Characteristics



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Ratings and Characteristics Curves

(T_A = 25°C unless otherwise specified)

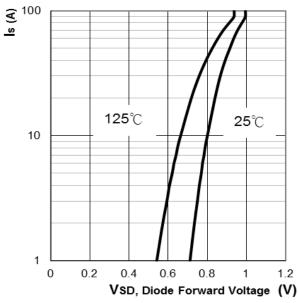


Figure 9: Body Diode Characters

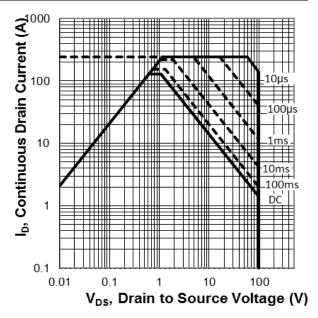


Figure 10: Maximum Safe Operation Area

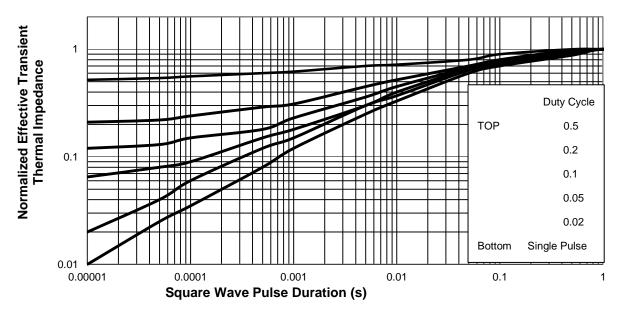


Figure 11: Normalized Thermal Transient Impedance, Junction-to-Case



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3. Marking information

Top Marking Rule

PFC PRM
4R7N10N5
YYWW ABSH

PRM4R7N10N5 = Product Type Marking Code

YYWW = Date Code

YY = Last two digits of year

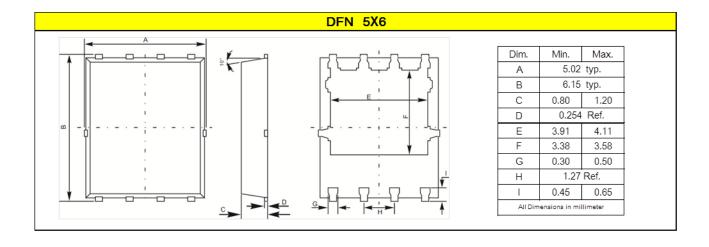
WW = Week code

ABS = Assembly code

H = Halogen Free (N/A = common molding compound)

4. Package information

Package Outline Dimensions millimeters





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5. Ordering information

Part Number	Package	Delivery mode
PRM4R7N10N5	DFN 5X6	3000 pcs / 13" diameter reel

Mechanical

Molder Plastic: UL Flammability Classification Rating 94V-0

■ Device Weight: 0.003 ounces (0.093grams) – DFN 5x6

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