

PRM5R2N03D

PFC Device Corporation

30V Single N-Channel MOSFET

Major ratings and characteristics

Characteristics	Values	Units
V _{DS}	30	٧
$I_{D}^{4} (T_{C}=25^{\circ}C)$	79	Α
Max. R _{DS(ON)} @V _{GS} =10V	5.2	mΩ
Max. R _{DS(ON)} @V _{GS} =4.5V	9.0	mΩ
T _J Operating Junction Temperature	-55 to +150	°C

General Description

The N-Channel enhancement mode power field effect transistor is using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. The device is well suited for high efficiency fast switching applications.

PRM5R2N03D TO-252 (D-PAK)

Typical Applications

- Charger Adapter
- Power Tools
- LED Lighting

Features

- Max. $R_{DS(ON)}=5.2m\Omega@V_{GS}=10V$
- Improved dv/dt capability
- Fast switching
- 100% E_{AS} Guaranteed
- Green Device Available

1. Characteristics

Maximum Ratings Characteristics

($T_A = 25$ °C unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	±20	V
l _D ⁴	Drain Current – Continuous (T _C =25°C)	79	Α
ID	Drain Current – Continuous (T _C =100°C)	50	Α
I_D^5	Drain Current – Continuous (T _C =25°C)	45	Α
I_{DM}	Drain Current – Pulsed ¹	180	Α
E _{AS}	Single Pulse Avalanche Energy ²	14.7	mJ
I _{AS}	Single Pulse Avalanche Current ²	10.8	Α
P _D	Power Dissipation (T _C =25°C)	54	W
ГD	Power Dissipation – Derate above 25°C	0.4	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T_J	T _J Operating Junction Temperature Range		°C

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
$R_{ hetaJA}$	Thermal Resistance Junction to ambient		62	°C/W
$R_{ heta JC}$	Thermal Resistance Junction to Case		2.3	°C/W



Electrical Characteristics

($T_J = 25$ °C unless otherwise specified)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	30			>
		V _{DS} =30V, V _{GS} =0V, T _J =25°C			1	uA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =24V,V _{GS} =0V, T _J =125°C			250	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA

On Characteristics

R _{DS(ON)} Static Drain-Source On-Resistance	V _{GS} =10V, I _D =20A		4.4	5.2	mΩ	
R _{DS(ON)}		V _{GS} =4.5V, I _D =10A		5.2	9.0	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_{D}=250uA$	1.0	1.6	2.5	V
g _{fs}	Forward Transconductance	V_{DS} =5V, I_D =10A		43		S

Dynamic and switching Characteristics

Qg	Total Gate Charge	V _{DS} =15V, V _{GS} =10V, I _D =20A		20.1	
Q_gs	Gate-Source Charge			3.7	 nC
Q_{gd}	Gate-Drain Charge			4.4	
$T_{d(on)}$	Turn-On Delay Time			11	
T _r	Turn-On Rise Time	V_{DD} =15V, V_{GS} =10V, R_{G} =3 Ω I_{D} =20A		87	 ns
$T_{d(off)}$	Turn-Off Delay Time			22	 113
T_f	Turn-Off Fall Time			6	
C_{iss}	Input Capacitance			1065	
C _{oss}	Output Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz		509	 pF
C _{rss}	Reverse Transfer Capacitance		-	45	
R_{g}	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz		1.6	 Ω

Drain-Source Diode Characteristics

V_{SD}^{3}	Source to Drain Diode Voltage	$V_{GS}=0V$, $I_{S}=20A$	 	1.1	V
t _{rr}	Reverse Recovery Time	1 201 di/dt 1001/up	 24		ns
Q_{rr}	Reverse Recovery Charge	I _S =20A, di/dt=100A/us	 13		nC

Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. L=0.1mH, $R_G=25\Omega$, Starting $T_J=25^{\circ}C$
- 3. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 4. Silicon limited.
- 5. Package limited.



2. Characteristics Curves

Ratings and Characteristics Curves

(T_A = 25°C unless otherwise specified)

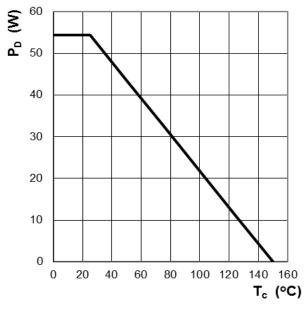


Figure 1: Power Dissipation

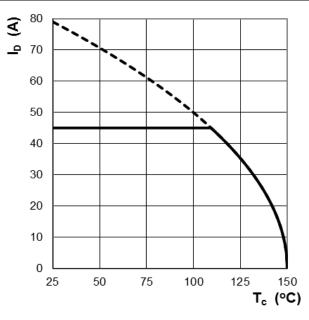


Figure 2: Continuous Drain Current vs. T_C

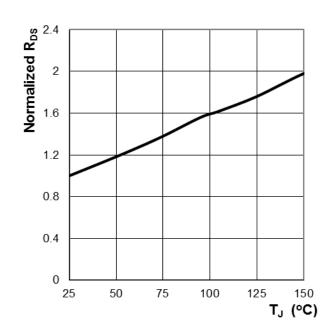


Figure 3: Normalized R_{DS(ON)} vs. T_J

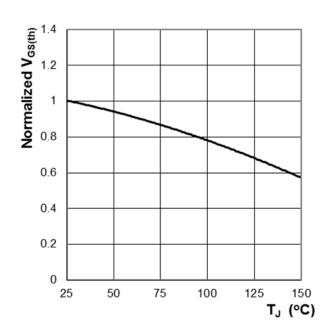


Figure 4: Normalized Vth vs. T_J



Ratings and Characteristics Curves

($T_A = 25^{\circ}C$ unless otherwise specified)

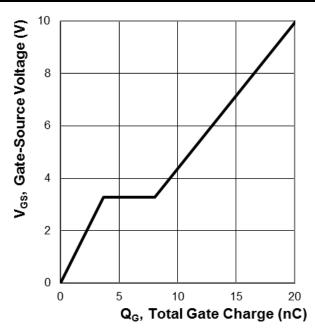


Figure 5: Typ. Gate Charge Characteristics

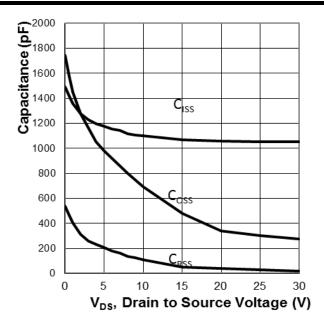


Figure 6: Typ. Capacitance Characteristics

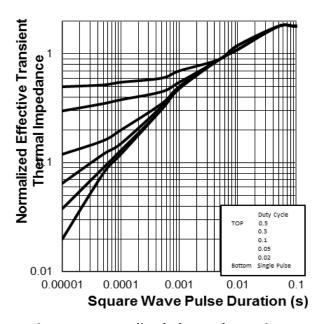


Figure 7: Normalized Thermal Transient Impedance, Junction-to-Case

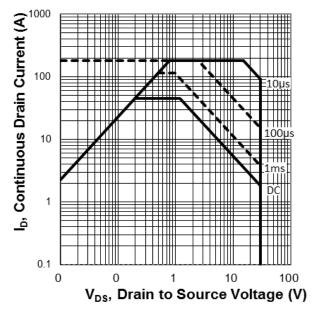


Figure 8: Maximum Safe Operation Area



3. Marking information

Top Marking Rule

PFC PRM
5R2N03D
YYWW ABSH

PRM5R2N03D = Product Type Marking Code

YYWW = Date Code

YY = Last two digits of year

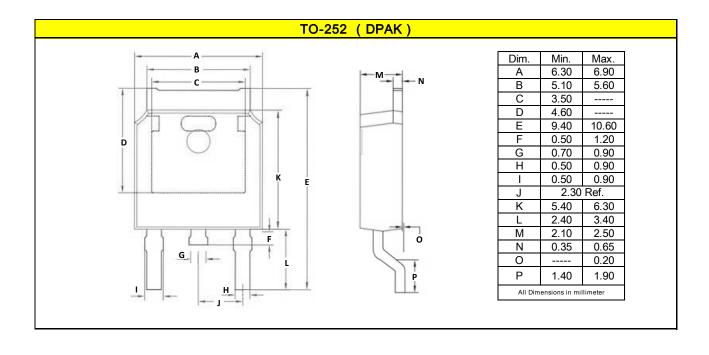
WW = Week code

ABS = Assembly code

H = Halogen Free (N/A = common molding compound)

4. Package information

Package Outline Dimensions millimeters





5. Ordering information

Part Number	Package	Delivery mode
PRM5R2N03D	TO-252 (D-PAK)	2500 pcs / 13" diameter reel

Mechanical

Molder Plastic: UL Flammability Classification Rating 94V-0
 Device Weight: 0.01 ounces (0.3grams) - TO-252 (D-PAK)

PFC Device Corp reserves the right to make changes without further notice to any products herein. PFC Device Corp makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does PFC Device Corp assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in PFC Device Corp data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typical" must be validated for each customer application by customer's technical experts. PFC Device Corp does not convey any license under its patent rights nor the rights of others. PFC Device Corp products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the PFC Device Corp product could create a situation where personal injury or death may occur. Should Buyer purchase or use PFC Device Corp products for any such unintended or unauthorized application, Buyer shall indemnify and hold PFC Device Corp and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that PFC Device Corp was negligent regarding the design or manufacture of the part.

