

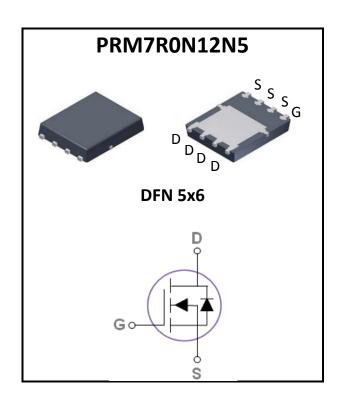
PRM7RON12N5

PFC Device Corporation

120V Single N-Channel MOSFET

Major ratings and characteristics

Characteristics	Values	Units
V_{DS}	120	٧
$I_D^4 (T_C=25^{\circ}C)$	101	Α
Max. R _{DS(ON)} @V _{GS} =10V	7	mΩ
Max. R _{DS(ON)} @V _{GS} =4.5V	9.8	mΩ
T _J Operating Junction Temperature	-55 to +150	°C



General Description

The N-Channel enhancement mode power field effect transistor is using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. The device is well suited for high efficiency fast switching applications.

Typical Applications

- Charger Adapter
- Power Tools
- LED Lighting

Features

- Max. $R_{DS(ON)}=7m\Omega@V_{GS}=10V$
- Improved dv/dt capability
- Fast switching
- 100% E_{AS} Guaranteed
- Green Device Available

Apr-2021 Version 4.1 1 / 8

1. Characteristics

Maximum Ratings Characteristics

($T_A = 25$ °C unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	120	V
V_{GS}	Gate-Source Voltage	±20	V
I_D^4	Drain Current – Continuous (T _C =25°C)	101	А
ID	Drain Current – Continuous (T _C =100°C)	65	Α
ID ⁵	Drain Current – Continuous (TC=25°C)	60	А
I _{DM}	Drain Current – Pulsed ¹	240	А
E _{AS}	Single Pulse Avalanche Energy ²	88	mJ
I_{AS}	Single Pulse Avalanche Current ²	21	А
В	Power Dissipation (T _C =25°C)	114	W
P _D	Power Dissipation – Derate above 25°C	0.90	W/°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient		55	°C/W
$R_{ heta JC}$	Thermal Resistance Junction to Case		1.1	°C/W



Version 4.1 2 / 8

Electrical Characteristics

(T_J = 25 °C unless otherwise specified)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	120			V
	Duein Course Looks as Course	V _{DS} =120V, V _{GS} =0V, T _J =25°C			1	uA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =96V, V _{GS} =0V, T _J =100°C			100	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA

On Characteristics

D	Static Drain-Source On-Resistance	V_{GS} =10V, I_D =20A		5.5	7	$m\Omega$
$R_{DS(ON)}$		V_{GS} =4.5V, I_{D} =20A		7.5	9.8	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_{D}=250uA$	1.0	1	2.5	>
g_{fs}	Forward Transconductance	V_{DS} =10V, I_{D} =5A	1	59	ł	S

Dynamic and switching Characteristics

Q_q	Total Gate Charge		 46	
Q_{qs}	Gate-Source Charge	V_{DS} =60V, V_{GS} =10V, I_{D} =20A	 12	 nC
Q_gd	Gate-Drain Charge		 5	
T _{d(on)}	Turn-On Delay Time	V_{DD} =60V, V_{GS} =10V, R_{G} =10 Ω I_{D} =20A	 22	
T _r	Turn-On Rise Time		 70	 nc
$T_{d(off)}$	Turn-Off Delay Time		 73	 ns
T_f	Turn-Off Fall Time		 112	
C _{iss}	Input Capacitance		 3485	
C _{oss}	Output Capacitance	V _{DS} =60V, V _{GS} =0V, f=1MHz	 431	 pF
C_{rss}	Reverse Transfer Capacitance		 24	
R_{g}	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz	 2.5	 Ω

Drain-Source Diode Characteristics

V_{SD}^{3}	Source to Drain Diode Voltage	V_{GS} =0V, I_{S} =20A			1.2	V
t _{rr}	Reverse Recovery Time	I _s =20A, di/dt=100A/us	-	63		ns
Q _{rr}	Reverse Recovery Charge	11 _S =20A, ui/ui=100A/uS		98		nC

Note:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2. L=0.4mH, $R_G=25\Omega$, Starting $T_J=25^{\circ}C$
- 3. The data tested by pulsed, pulse width \leq 300us, duty cycle \leq 2%.
- 4. Silicon limited.
- 5. Package limited.



Version 4.1 3 / 8

2. Characteristics Curves

Ratings and Characteristics Curves

($T_A = 25^{\circ}C$ unless otherwise specified)

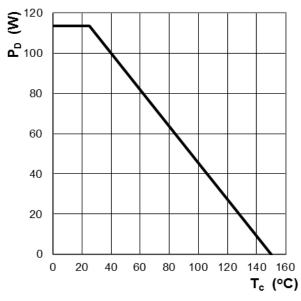


Figure 1: Power Dissipation

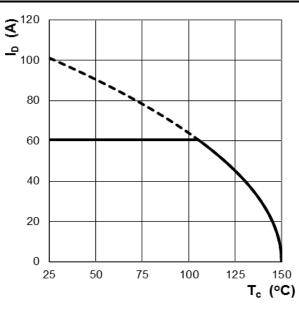


Figure 2: Continuous Drain Current vs. T_C

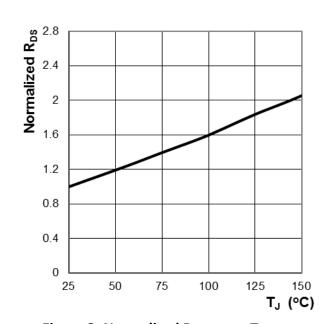


Figure 3: Normalized R_{DS(ON)} vs. T_J

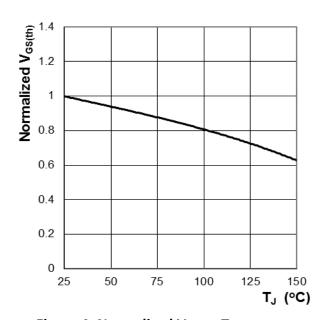


Figure 4: Normalized V_{th} vs. T_J



Version 4.1 4 / 8

Ratings and Characteristics Curves

(T_A = 25°C unless otherwise specified)

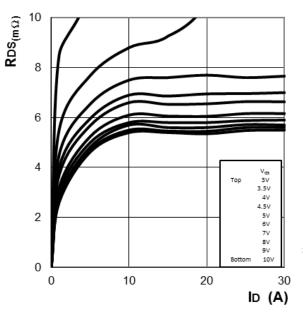


Figure 5: RDS(ON) vs. Drain Current and Gate Voltage

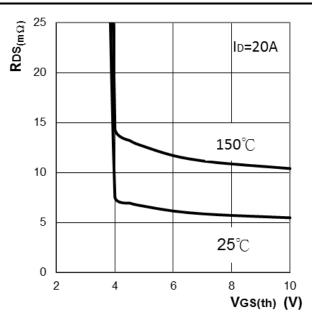


Figure 6: RDS(ON) vs. Gate Voltage

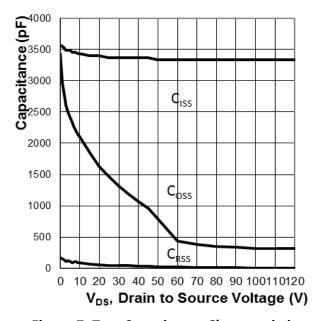


Figure 7: Typ. Capacitance Characteristics

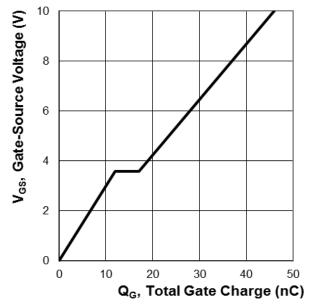


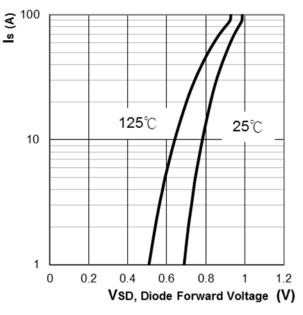
Figure 8: Typ. Gate Charge Characteristics



Version 4.1 5 / 8

Ratings and Characteristics Curves

(T_A = 25°C unless otherwise specified)



Continuo (A) 100 (D) 1 (D) 100 (

Figure 9: Body Diode Characters

Figure 10: Maximum Safe Operation Area

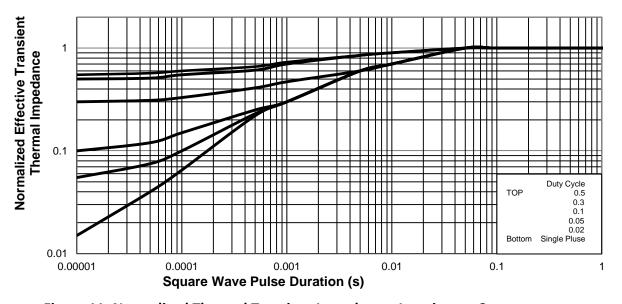


Figure 11: Normalized Thermal Transient Impedance, Junction-to-Case



Version 4.1 6 / 8

3. Marking information

Top Marking Rule

PFC PRM
7R0N12N5
YYWW ABSH

PRM7R0N12N5 = Product Type Marking Code

YYWW = Date Code

YY = Last two digits of year

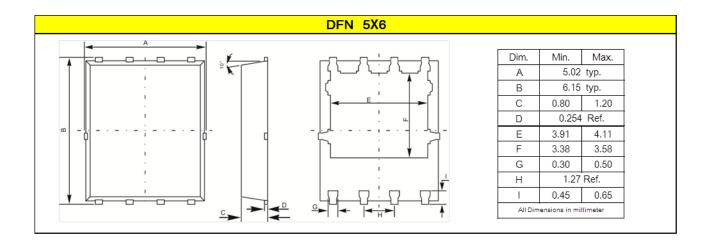
WW = Week code

ABS = Assembly code

H = Halogen Free (N/A = common molding compound)

4. Package information

Package Outline Dimensions millimeters





Version 4.1 7 / 8

5. Ordering information

Part Number	Package	Delivery mode
PRM7R0N12N5	DFN 5X6	3000 pcs / 13" diameter reel

Mechanical

Molder Plastic: UL Flammability Classification Rating 94V-0
 Device Weight: 0.003 ounces (0.093grams) – DFN 5x6

PFC Device Corp reserves the right to make changes without further notice to any products herein. PFC Device Corp makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does PFC Device Corp assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in PFC Device Corp data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typical" must be validated for each customer application by customer's technical experts. PFC Device Corp does not convey any license under its patent rights nor the rights of others. PFC Device Corp products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the PFC Device Corp product could create a situation where personal injury or death may occur. Should Buyer purchase or use PFC Device Corp products for any such unintended or unauthorized application, Buyer shall indemnify and hold PFC Device Corp and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that PFC Device Corp. was negligent regarding the design or manufacture of the part.



Version 4.1 8 / 8