

# PRM7R4N06D

# **PFC Device Corporation**

# **60V Single N-Channel MOSFET**

## Major ratings and characteristics

Characteristics	Values	Units
$V_{DS}$	60	٧
$I_D^5 (T_C=25^{\circ}C)$	94	Α
Max. R <sub>DS(ON)</sub> @V <sub>GS</sub> =10V	7.4	mΩ
Max. R <sub>DS(ON)</sub> @V <sub>GS</sub> =4.5V	11.4	mΩ
T <sub>J</sub> Operating Junction Temperature	-55 to +150	°C

# **General Description**

The N-Channel enhancement mode power field effect transistor is using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. The device is well suited for high efficiency fast switching applications.

# PRM7R4N06D TO-252 (D-PAK)

# **Typical Applications**

- Charger Adapter
- Power Tools
- LED Lighting

#### **Features**

- Max. R<sub>DS(ON)</sub>=7.4mΩ@V<sub>GS</sub>=10V
- Improved dv/dt capability
- Fast switching
- 100% E<sub>AS</sub> Guaranteed
- Green Device Available

# 1. Characteristics

# **Maximum Ratings Characteristics**

(  $T_A = 25$  °C unless otherwise specified )

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	60	V
$V_{GS}$	Gate-Source Voltage	±20	V
$I_D^{5}$	Drain Current – Continuous (T <sub>C</sub> =25°C)	94	Α
ID	Drain Current – Continuous (T <sub>C</sub> =100°C)	59	А
$I_D^6$	Drain Current – Continuous (T <sub>C</sub> =25°C)	60	Α
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	240	Α
$E_AS$	Single Pulse Avalanche Energy <sup>2</sup>	28	mJ
I <sub>AS</sub>	Single Pulse Avalanche Current <sup>2</sup>	24	Α
D	Power Dissipation (T <sub>C</sub> =25°C)	102	W
$P_D$	Power Dissipation – Derate above 25°C	0.8	W/°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient		62	°C/W
$R_{ heta JC}$	Thermal Resistance Junction to Case		1.22	°C/W



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#### **Electrical Characteristics**

( $T_J = 25$  °C unless otherwise specified)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	60			>
	Drain Source Leekage Current	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C			1	uA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C			250	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±100	nA

#### **On Characteristics**

R <sub>DS(ON)</sub> Static Drain-Source On-Resistance	$V_{GS}$ =10V, $I_D$ =20A		6.2	7.4	mΩ	
$R_{DS(ON)}$	Static Dialii-Source On-Nesistance	V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A		9.6	11.4	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.0	1.7	2.5	V
$g_{\sf fs}$	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =20A		42		S

#### **Dynamic and switching Characteristics**

$Q_{g}$	Total Gate Charge <sup>3, 4</sup>	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =10A	 27.6		
$Q_{qs}$	Gate-Source Charge <sup>3, 4</sup>		 5.7		nC
$Q_{gd}$	Gate-Drain Charge <sup>3, 4</sup>		 4.7		
$T_{d(on)}$	Turn-On Delay Time <sup>3, 4</sup>		 12		
T <sub>r</sub>	Turn-On Rise Time <sup>3, 4</sup>	$V_{DD}$ =30V, $V_{GS}$ =10V, $R_{G}$ =6 $\Omega$	 20		ns
$T_{d(off)}$	Turn-Off Delay Time <sup>3, 4</sup>	I <sub>D</sub> =1A	 36		115
$T_f$	Turn-Off Fall Time <sup>3, 4</sup>		 44	-	
C <sub>iss</sub>	Input Capacitance		 1651		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz	 531		pF
$C_{rss}$	Reverse Transfer Capacitance		 42		
$R_{g}$	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	 1.0		Ω

#### **Drain-Source Diode Characteristics**

V	'SD	Source to Drain Diode Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =1A	 	1.0	V
t	t <sub>rr</sub>	Reverse Recovery Time	1 201 di/dt 1001/up	 21		ns
C	J <sup>rr</sup>	Reverse Recovery Charge	I <sub>S</sub> =20A, di/dt=100A/us	 9		nC

#### Note :

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2.  $V_{DD}$ =50V,  $V_{GS}$ =10V, L=0.1mH,  $R_{G}$ =25 $\Omega$ , Starting  $T_{J}$ =25 $^{\circ}$ C
- 3. The data tested by pulsed , pulse width  $\leq$ 300us , duty cycle  $\leq$ 2%.
- 4. Essentially independent of operating temperature.
- 5. Silicon limited.
- 6. Package limited.



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# 2. Characteristics Curves

## **Ratings and Characteristics Curves**

# ( T<sub>A</sub> = 25° unless otherwise specified )

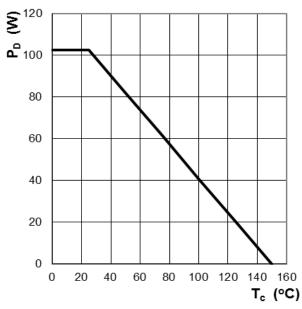


Figure 1: Power Dissipation

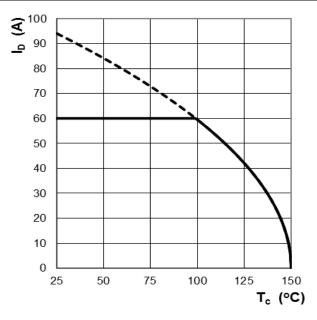


Figure 2: Continuous Drain Current vs. T<sub>C</sub>

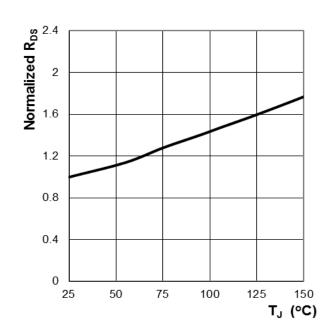


Figure 3: Normalized R<sub>DS(ON)</sub> vs. T<sub>J</sub>

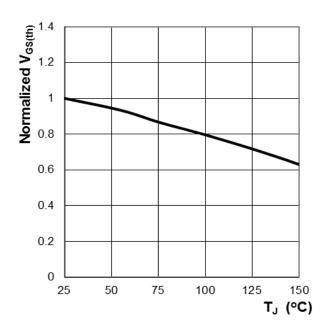


Figure 4: Normalized V<sub>GS(th)</sub> vs. T<sub>J</sub>



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#### **Ratings and Characteristics Curves**

## ( T<sub>A</sub> = 25°C unless otherwise specified )

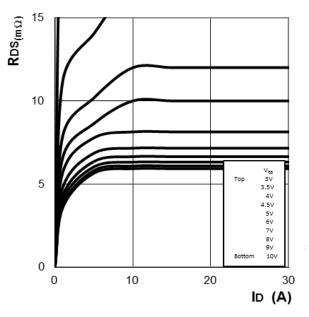


Figure 5: RDS(ON) vs. Drain Current and Gate Voltage

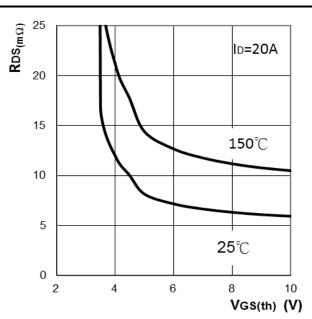


Figure 6: RDS(ON) vs. Gate Voltage

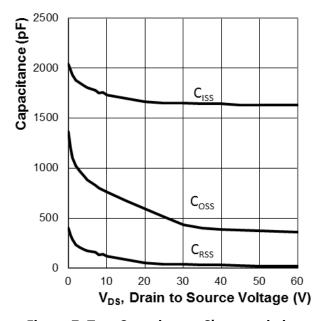


Figure 7: Typ. Capacitance Characteristics

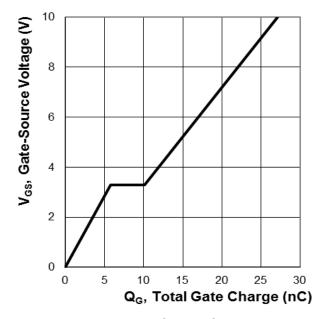


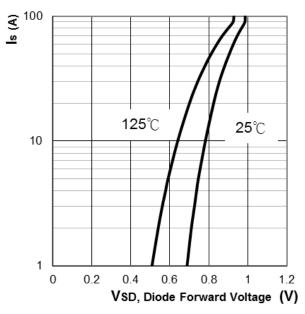
Figure 8: Typ. Gate Charge Characteristics



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#### **Ratings and Characteristics Curves**

#### ( $T_A = 25^{\circ}C$ unless otherwise specified)



**Figure 9: Body Diode Characters** 

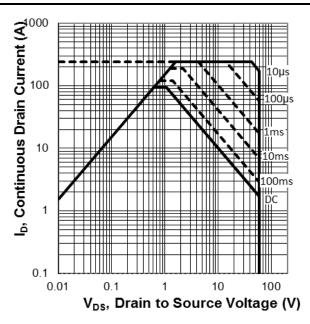


Figure 10: Maximum Safe Operation Area

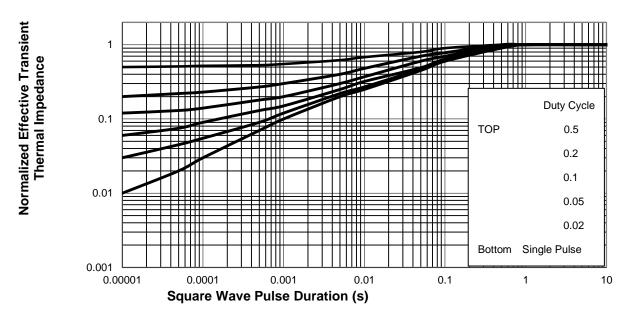


Figure 11: Normalized Thermal Transient Impedance, Junction-to-Case



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# 3. Marking information

**Top Marking Rule** 

PFC PRM 7R4N06D YYWW ABSH PRM7R4N06D = Product Type Marking Code

YYWW = Date Code

YY = Last two digits of year

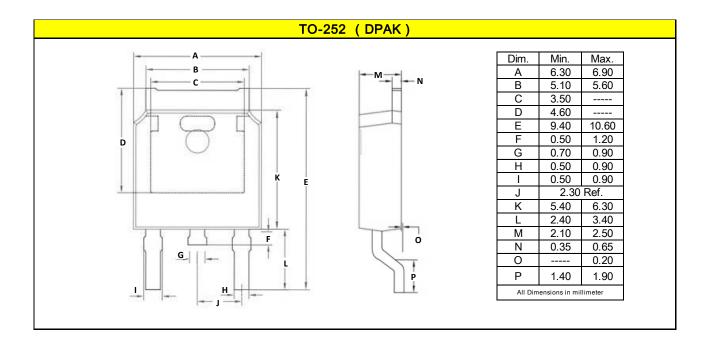
WW = Week code

ABS = Assembly code

H = Halogen Free (N/A = common molding compound)

# 4. Package information

Package Outline Dimensions millimeters





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### 5. Ordering information

Part Number	Package	Delivery mode
PRM7R4N06D	TO-252 (D-PAK)	2500 pcs / 13" diameter reel

#### Mechanical

Molder Plastic: UL Flammability Classification Rating 94V-0

Device Weight: 0.01 ounces (0.3grams) - TO-252 (D-PAK)

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