

PRM9R7N10D

PFC Device Corporation

100V Single N-Channel MOSFET

Major ratings and characteristics

Characteristics	Values	Units
V_{DS}	100	٧
$I_{D}^{5} (T_{C}=25^{\circ}C)$	71	Α
Max. R _{DS(ON)} @V _{GS} =10V	9.7	mΩ
Max. R _{DS(ON)} @V _{GS} =4.5V	14	mΩ
T _J Operating Junction Temperature	-55 to +150	°C

General Description

The N-Channel enhancement mode power field effect transistor is using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. The device is well suited for high efficiency fast switching applications.

PRM9R7N10D TO-252 (D-PAK)

Typical Applications

- Charger Adapter
- Power Tools
- LED Lighting

Features

- Max. R_{DS(ON)}=9.7mΩ@V_{GS}=10V
- Improved dv/dt capability
- Fast switching
- 100% E_{AS} Guaranteed
- Green Device Available

1. Characteristics

Maximum Ratings Characteristics

($T_A = 25$ °C unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	±20	V
I _D ⁵	Drain Current – Continuous (T _C =25°C)	71	Α
ID	Drain Current – Continuous (T _C =100°C)	45	Α
I_D^6	Drain Current – Continuous (T _C =25°C)	50	Α
I _{DM}	Drain Current – Pulsed ¹	14.6	Α
E _{AS}	Single Pulse Avalanche Energy ²	32	mJ
I _{AS}	Single Pulse Avalanche Current ²	25	Α
D	Power Dissipation (T _C =25°C)	73	W
P _D	Power Dissipation – Derate above 25°C	0.59	W/°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient		50	°C/W
$R_{ heta JC}$	Thermal Resistance Junction to Case		1.7	°C/W



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Electrical Characteristics

(T_J = 25 °C unless otherwise specified)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	100			V
-	Davis Comment and an a Comment	V _{DS} =100V, V _{GS} =0V, T _J =25°C			1	uA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =80V, V _{GS} =0V, T _J =85°C			100	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA

On Characteristics

	R _{DS(ON)} Static Drain-Source On-Resistance	$V_{GS}=10V$, $I_{D}=13A$		8.0	9.7	mΩ	
		V _{GS} =4.5V, I _D =8A		11	15	mΩ	
	$V_{GS(th)}$	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.0	1.7	2.5	V
	g_{fs}	Forward Transconductance	V _{DS} =5V, I _D =10A		38	I	S

Dynamic and switching Characteristics

Q_q	Total Gate Charge ^{3,4}		 17	
Q_{qs}	Gate-Source Charge ^{3, 4}	V_{DS} =50V, V_{GS} =4.5V, I_{D} =13A	 4.9	 nC
Q_{gd}	Gate-Drain Charge ^{3, 4}		 8.2	
$T_{d(on)}$	Turn-On Delay Time ^{3, 4}		 12	
T _r	Turn-On Rise Time ^{3,4}	V_{DD} =50V, V_{GS} =10V, R_{G} =3 Ω I_{D} =13A	 50	 nc
$T_{d(off)}$	Turn-Off Delay Time ^{3, 4}		 29	 ns
T_f	Turn-Off Fall Time ^{3, 4}		 69	
C _{iss}	Input Capacitance		 1609	
C _{oss}	Output Capacitance	V _{DS} =50V, V _{GS} =0V, f=1MHz	 296	 pF
C_{rss}	Reverse Transfer Capacitance		 30	
R_{g}	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz	 1.0	 Ω

Drain-Source Diode Characteristics

V_{SD}	Source to Drain Diode Voltage	$V_{GS}=0V$, $I_{S}=1A$	 	1.2	V
t _{rr}	Reverse Recovery Time	1 _12	 46		ns
Q_{rr}	Reverse Recovery Charge	I _s =13A, di/dt=100A/us	 44		nC

Note:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2. VDD=50V, VGS=10V, L=0.1mH, RG=25 Ω , Starting TJ=25 $^{\circ}$ C
- 3. The data tested by pulsed , pulse width ≤300us , duty cycle ≤2%.
- 4. Essentially independent of operating temperature.
- 5. Silicon limited.
- Package limited.

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2. Characteristics Curves

Ratings and Characteristics Curves

(T_A = 25°C unless otherwise specified)

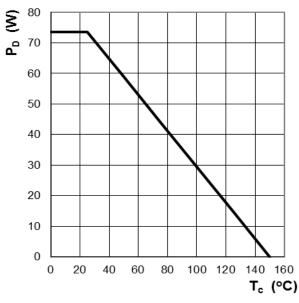


Figure 1: Power Dissipation

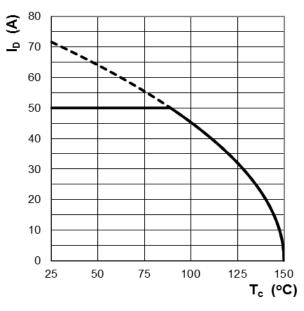


Figure 2: Continuous Drain Current vs. T_C

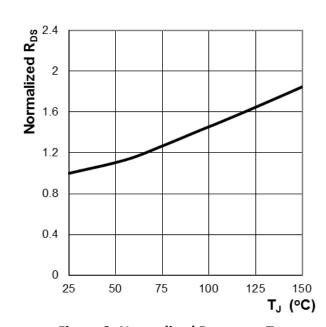


Figure 3: Normalized R_{DS(ON)} vs. T_J

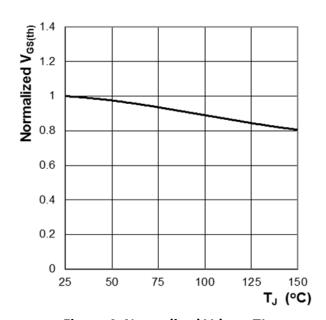


Figure 4: Normalized Vth vs. TJ



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Ratings and Characteristics Curves

($T_A = 25^{\circ}C$ unless otherwise specified)

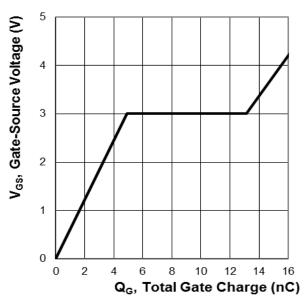


Figure 5: Typ. Gate Charge Characteristics

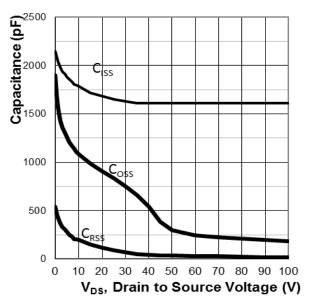


Figure 6: Typ. Capacitance Characteristics

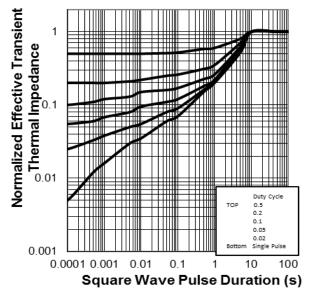


Figure 7: Normalized Thermal Transient Impedance, Junction-to-Case

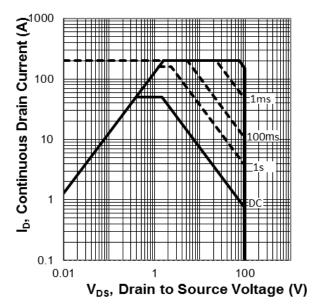


Figure 8: Maximum Safe Operation Area



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3. Marking information

Top Marking Rule

PFC PRM 9R7N10D YYWW ABSH PRM9R7N10D = Product Type Marking Code

YYWW = Date Code

YY = Last two digits of year

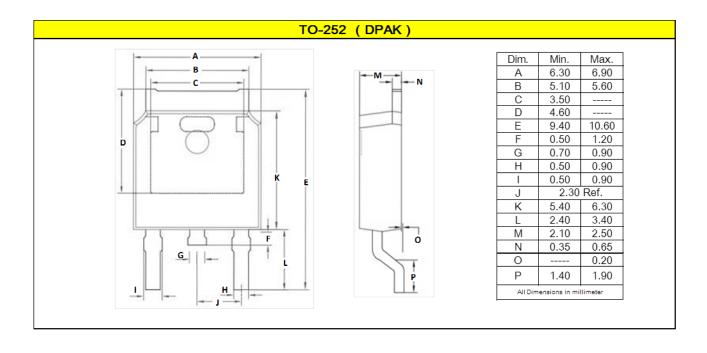
WW = Week code

ABS = Assembly code

H = Halogen Free (N/A = common molding compound)

4. Package information

Package Outline Dimensions millimeters





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5. Ordering information

Part Number	Package	Delivery mode
PRM9R7N10D	TO-252 (D-PAK)	2500 pcs / 13" diameter reel

Mechanical

Molder Plastic: UL Flammability Classification Rating 94V-0
 Device Weight: 0.01 ounces (0.3grams) - TO-252 (D-PAK)

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