

PRM9R7N10N5

PFC Device Corporation

100V Single N-Channel MOSFET

Major ratings and characteristics

Characteristics	Values	Units
V_{DS}	100	٧
$I_{D}^{6} (T_{C}=25^{\circ}C)$	60	Α
Max. R _{DS(ON)} @V _{GS} =10V	9.7	mΩ
Max. R _{DS(ON)} @V _{GS} =4.5V	15	mΩ
T _J Operating Junction Temperature	-55 to +150	°င

General Description

The N-Channel enhancement mode power field effect transistor is using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. The device is well suited for high efficiency fast switching applications.

PRM9R7N10N5 DFN 5x6

Typical Applications

- Charger Adapter
- Power Tools
- LED Lighting

Features

- Max. $R_{DS(ON)}=9.7m\Omega@V_{GS}=10V$
- Improved dv/dt capability
- Fast switching
- 100% E_{AS} Guaranteed
- Green Device Available

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1. Characteristics

Maximum Ratings Characteristics

(T_A = 25 °C unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	±20	V
I _D ⁵	Drain Current – Continuous (T _C =25°C)	74	Α
ID	Drain Current – Continuous (T _C =100°C)	46	А
I _D ⁶	Drain Current – Continuous (TC=25°C)	60	Α
I _{DM}	Drain Current – Pulsed ¹	240	А
E _{AS}	Single Pulse Avalanche Energy ²	32	mJ
I _{AS}	Single Pulse Avalanche Current ²	25	Α
В	Power Dissipation (T _C =25°C)	78	W
P _D	Power Dissipation – Derate above 25°C	0.63	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
$R_{ heta JA}$	Thermal Resistance Junction to ambient		60	°C/W
Raic	Thermal Resistance Junction to Case		1.6	°C/W



Electrical Characteristics

($T_J = 25$ °C unless otherwise specified)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	100			V
		V _{DS} =100V, V _{GS} =0V, T _J =25°C			1	uA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =80V, V _{GS} =0V, T _J =85°C			100	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA

On Characteristics

R _{DS(ON)} Static Drain-Source On-Resistance	V _{GS} =10V, I _D =13A			9.7	mΩ	
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =4.5V, I _D =8A			15	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.0	1.8	2.5	V
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =10A		36		S

Dynamic and switching Characteristics

Q_{g}	Total Gate Charge ^{3,4}	V _{DS} =50V, V _{GS} =4.5V, I _D =13A	 17		
Q_{qs}	Gate-Source Charge ^{3, 4}		 5.5		nC
Q_gd	Gate-Drain Charge ^{3, 4}		 7.6		
$T_{d(on)}$	Turn-On Delay Time ^{3,4}		 12		
T_r	Turn-On Rise Time ^{3,4}	V_{DD} =50V, V_{GS} =10V, R_{G} =3 Ω	 40		no
$T_{d(off)}$	Turn-Off Delay Time ^{3, 4}		 28		ns
T_f	Turn-Off Fall Time ^{3, 4}		 40	-	
C_{iss}	Input Capacitance		 1667		
C_{oss}	Output Capacitance	V _{DS} =50V, V _{GS} =0V, f=1MHz	 286		pF
C_{rss}	Reverse Transfer Capacitance		 31		
R_{g}	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz	 1.4		Ω

Drain-Source Diode Characteristics

V_{SD}	Source to Drain Diode Voltage	V _{GS} =0V, I _S =1A	 	1.2	V
t _{rr}	Reverse Recovery Time	1 12A di/dt 100A/ug	 52		ns
Q_{rr}	Reverse Recovery Charge	I _S =13A, di/dt=100A/us	 71		nC

Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. VDD=50V, VGS=10V, L=0.1mH, RG=25 Ω , Starting TJ=25 $^{\circ}$ C
- 3. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 4. Essentially independent of operating temperature.
- 5. Silicon limited.
- 6. Package limited.



2. Characteristics Curves

Ratings and Characteristics Curves

(T_A = 25° unless otherwise specified)

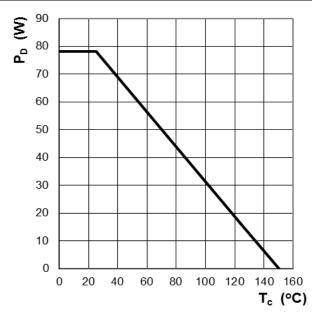


Figure 1: Power Dissipation

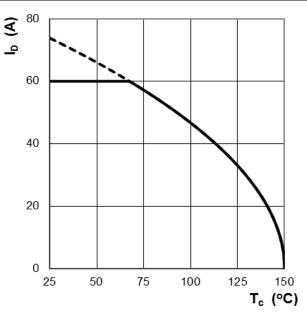


Figure 2: Continuous Drain Current vs. T_C

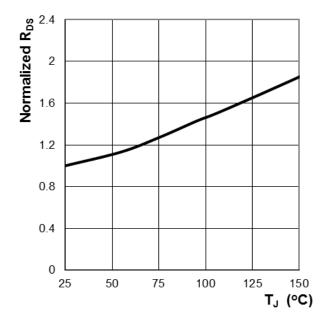


Figure 3: Normalized R_{DS(ON)} vs. T_J

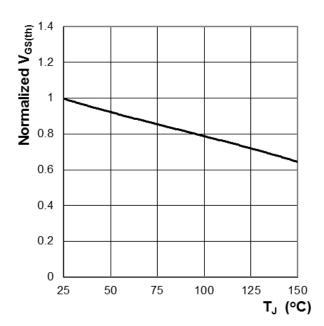


Figure 4: Normalized V_{GS(th)} vs. T_J



Ratings and Characteristics Curves

($T_A = 25^{\circ}C$ unless otherwise specified)

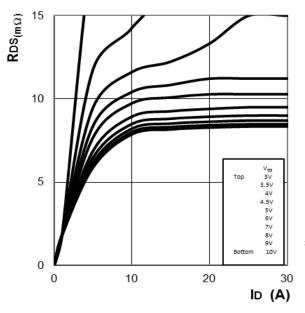


Figure 5: RDS(ON) vs. Drain Current and Gate Voltage

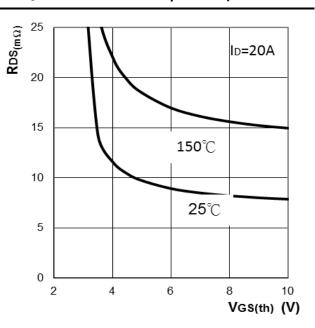


Figure 6: RDS(ON) vs. Gate Voltage

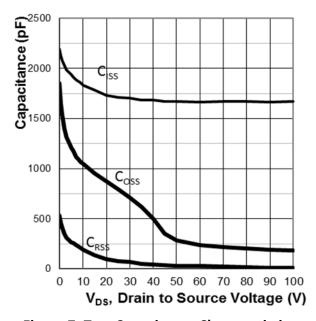


Figure 7: Typ. Capacitance Characteristics

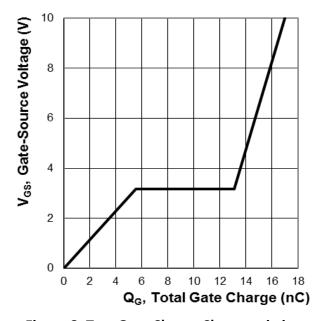
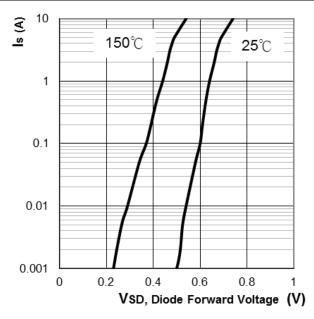


Figure 8: Typ. Gate Charge Characteristics





($T_A = 25^{\circ}C$ unless otherwise specified)



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Figure 9: Body Diode Characters

Figure 10: Maximum Safe Operation Area

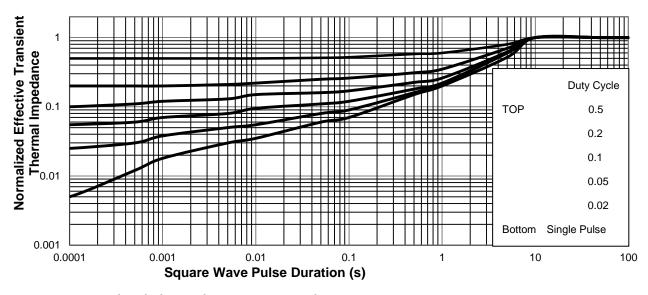


Figure 11: Normalized Thermal Transient Impedance, Junction-to-Case



3. Marking information

Top Marking Rule

PFC PRM
9R7N10N5
YYWW ABSH

PRM9R7N10N5 = Product Type Marking Code

YYWW = Date Code

YY = Last two digits of year

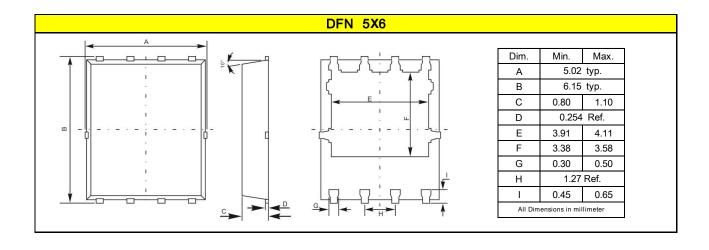
WW = Week code

ABS = Assembly code

H = Halogen Free (N/A = common molding compound)

4. Package information

Package Outline Dimensions millimeters





5. Ordering information

Part Number	Package	Delivery mode
PRM9R7N10N5	DFN 5X6	5000 pcs / 13" diameter reel

Mechanical

Molder Plastic: UL Flammability Classification Rating 94V-0

■ Device Weight: 0.003 ounces (0.093grams) – DFN 5x6

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