

# PSM20N65CTF

## PFC Device Corporation

## 20A 650V Single N-Channel Power MOSFET

## Major ratings and characteristics

Characteristics	Values	Units
$V_{DS}@T_Jmax$	650	V
$R_{DS(ON),}V_{GS}=10V$	0.19	Ω
I <sub>D</sub>	20	Α

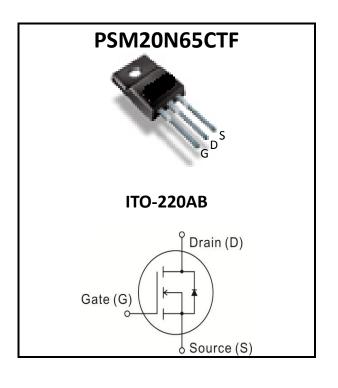
## **General Description**

PFC MLSJ (Multi-Layer Super Junction) MOSFET technology is the ideal choice for the PFC and PWM application. PFC device provides practical advantages of higher pressure-resistance, lower on-resistance to achieve the ideal balance between the switching speed and on-resistance.

## **Typical Applications**

PFC stages, hard switching PWM stages and resonant switching stages for PC, Adapter, LCD & PDP TV, Lighting, Server, Telecom and UPS.

Please note: For MOSFET paralleling the use of ferrite beads on the gate or separate totem poles is generally recommended.



### Features

- Advanced High Voltage Technology
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Extreme dv/dt rated
- Lead Free Finish, RoHS Compliant

### **1.** Characteristics

**Maximum Ratings Characteristics** 

( $T_A = 25$  °C unless otherwise specified)

Symbol	Parameter	Rating	Units
$V_{\text{DSS}}$	Drain-Source Voltage	650	V
	Drain Current – Continuous (T <sub>c</sub> =25°C)	20	А
I <sub>D</sub>	Drain Current – Continuous (T <sub>c</sub> =100°C)	13	А
I <sub>D pulsed</sub>	Pulsed Drain Current tp limited by $T_J$ max (Note 1)	60	А
E <sub>AS</sub>	Single Pulse Avalanche Energy (Noted 2)	300	mJ
E <sub>AR</sub>	Avalanche Energy, repetitive $t_{AR}$ limited by $T_{imax}$ (Note 3) $I_D=20A$ , $V_{DD}=50V$	1	mJ
I <sub>AR</sub>	Avalanche Current, repetitive t <sub>AR</sub> limited by T <sub>imax</sub>	20	А
$V_{GS}$	Gate-Source Voltage Static	±20	V
$V_{GS}$	Gate-Source Voltage AC (f>1Hz)	±30	V
P <sub>tot</sub>	Power Dissipation	34.5	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
dv/dt	Peak Diode Recovery dv/dt (Note 4)	15	V/nS
dv/dt	MOSFET dvdt ruggedness, V <sub>DS</sub> =480V	50	V/nS

#### **Thermal Characteristics**

Symbol	Parameter		Max.	Unit
$R_{ extsf{ heta}JA}$	Thermal Resistance Junction to ambient		80	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction to case (Drain)		3.6	°C/W



#### Electrical Characteristics

### ( $T_J = 25$ °C unless otherwise specified)

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Off Characteristics						
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	650			V
Dania Cau	Drain Source Lookage Current	V <sub>DS</sub> =600V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C		0.05	1	uA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =600V, V <sub>GS</sub> =0V, T <sub>J</sub> =150°C			100	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}=\pm 30V, V_{DS}=0V$			±100	nA

#### **On Characteristics**

D	R <sub>DS(ON)</sub> Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =13A, T <sub>J</sub> =25°C		0.165	0.190	Ω
RDS(ON)		V <sub>GS</sub> =10V, I <sub>D</sub> =13A, T <sub>J</sub> =150°C		0.45		Ω
V <sub>GS(th)</sub>	Gate-Source Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =1000uA	2.8	3.2	3.9	V
$R_{G}$	Gate input resistance	f=1MHz, open Drain		0.54		Ω

#### **Dynamic and switching Characteristics**

Q <sub>gs</sub>	Gate-Source Charge	V <sub>DD</sub> =480V, I <sub>D</sub> =20A,	 13		
$Q_qd$	Gate-Drain Charge		 33		nC
Q <sub>g</sub>	Gate charge total	V <sub>GS</sub> =0 to 10V	 75	110	
V <sub>(plateau)</sub>	Gate plateau voltage		 5.9		V
<b>g</b> <sub>fs</sub>	Transecondtance	$V_{DS} {\geq} 2^* I_D {}^* R_{DS(on)max}, I_D {=} 13A$	 20.5		S
T <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD}$ =380V, $V_{GS}$ =0/13V, $I_{D}$ =20A, $R_{g}$ =3.6 $\Omega$ , $T_{J}$ =25 °C	 8		
Tr	Rise Time		 34		nS
T <sub>d(off)</sub>	Turn-Off Delay Time		 42		113
T <sub>f</sub>	Fall Time		 58		
C <sub>iss</sub>	Input Capacitance	−V <sub>GS</sub> =0V, V <sub>DS</sub> =100V –f=1 MHz	 2400		
C <sub>oss</sub>	Output Capacitance		 81		pF
C <sub>rss</sub>	Reverse Transfer Capacitance		 32		

#### **Drain-Source Diode Characteristics and Maximum ratings**

$V_{SD}$	Inverse diode forward voltage	$I_{\rm S} = 20$ A, $V_{\rm GS} = 0$ V	 0.91	1.2	V
t <sub>rr</sub>	Reverse Recovery Time		 491	800	nS
Q <sub>rr</sub>	Reverse Recovery Charge	V <sub>R</sub> =480V, I <sub>F</sub> =I <sub>S</sub> , di⊧/dt=100A/uS	 10		uC
l <sub>rrm</sub>	Peak reverse recovery current		 42		А

Note :

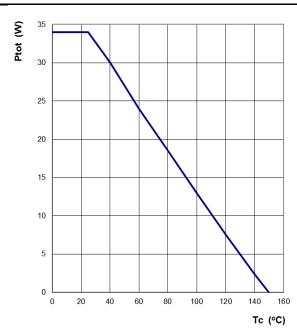
- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2.  $V_{DD}$ =50V,  $I_D$ =10A, Starting  $T_J$ =25°C.
- 3. Repetitive avalanche cause additional power lose that can be calculated as  $P_{AV}=E_{AR}*f$ .
- 4.  $I_{SD} <= I_D$ , di/dt <= 400 A/us,  $T_J < T_J$ , max

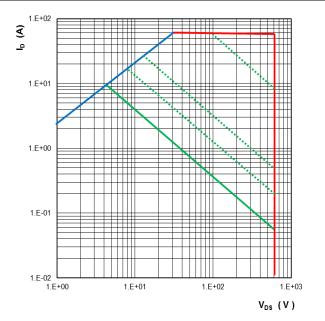


## 2. Characteristics Curves

**Ratings and Characteristics Curves** 

(  $T_A = 25^{\circ}C$  unless otherwise specified )





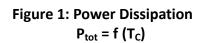


Figure 2: Safe operating area  $I_D = f(V_{DS})$ parameter : D = 0, T<sub>c</sub>=25°C

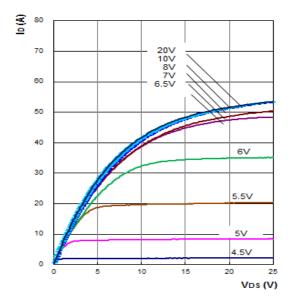


Figure 3: Typ. Output Characteristics  $I_D = f(V_{DS})$ ;  $T_J = 25^{\circ}C$ parameter :  $t_p = 100uS$ ,  $V_{GS}$ 

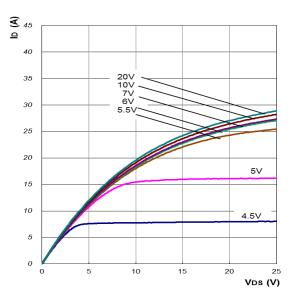
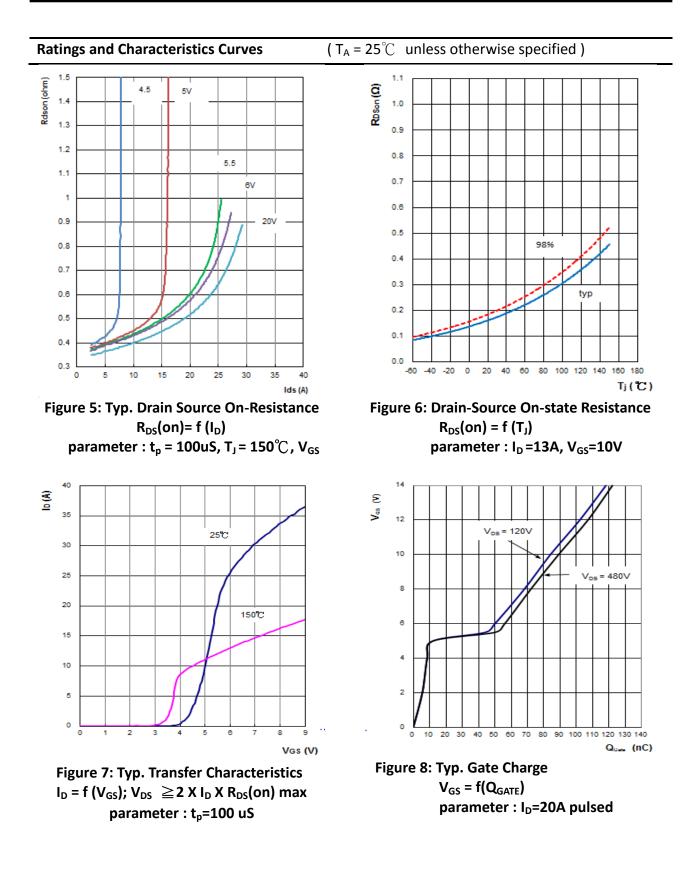


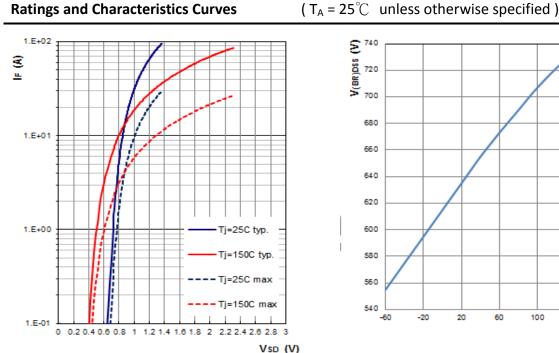
Figure 4: Typ. Output Characteristics  $I_D = f(V_{DS})$ ;  $T_J = 150^{\circ}C$ parameter :  $t_p = 100uS$ ,  $V_{GS}$ 

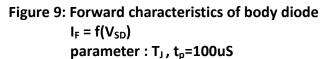


Version 4.1









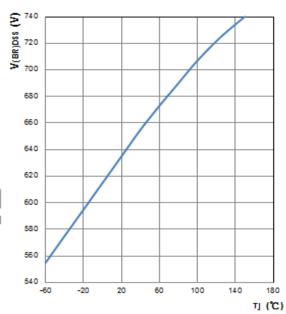


Figure 10: Drain-source breakdown voltage  $V_{(BR)DSS} = f(T_J)$ 

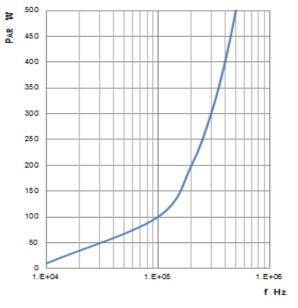


Figure 11: Avalanche power losses  $P_{AR} = f(f)$ parameter : E<sub>AR</sub>=1mJ

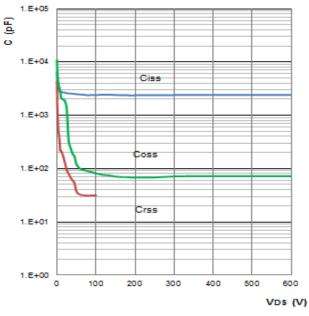


Figure 12: Typ. Capacitances  $C = f(V_{DS})$ parameter : V<sub>GS</sub>=0V, f=1MHz



Version 4.1

## **Characteristics**

## PSM20N65CTF

**Ratings and Characteristics Curves** ( $T_A = 25^{\circ}C$  unless otherwise specified)

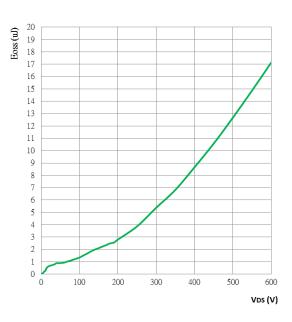


Figure 13: Typ.  $C_{oss}$  stored energy  $E_{oss}$ =f ( $V_{DS}$ )



## 3. Test Circuits and Waveforms

**Test Circuits and Waveforms** 

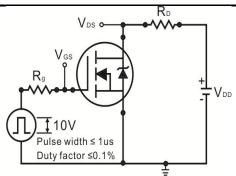


Figure 1: Switching times test circuit

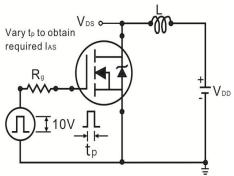
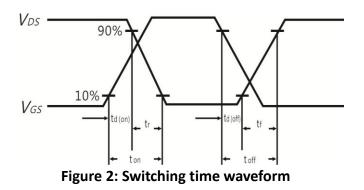


Figure 3: Unclamped test circuit



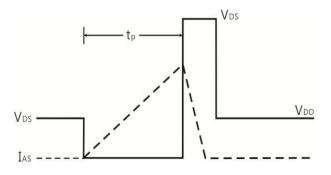


Figure 4: Unclamped test waveform

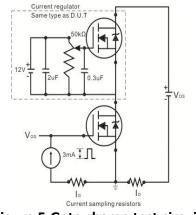


Figure 5:Gate charge test circuit

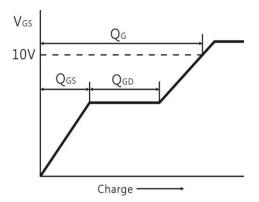
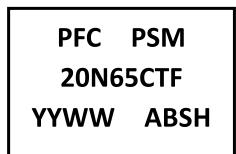


Figure 6: Basic gate charge waveform



## 4. Marking information

**Top Marking Rule** 



PSM20N65CTF = Product Type Marking Code

YY = Last two digits of year

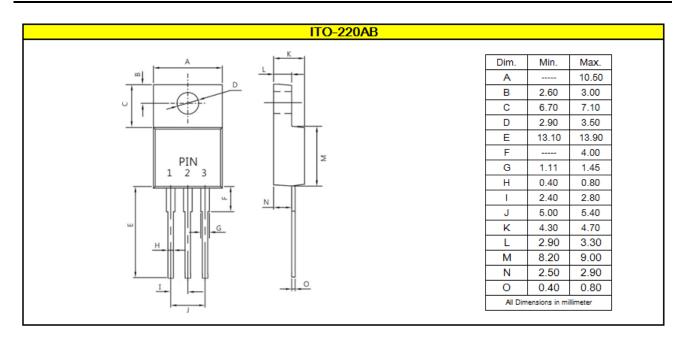
WW = Week code

ABS = Assembly code

H = Halogen Free (N/A = common molding compound)

## 5. Package information

Package Outline Dimensions millimeters





## 6. Ordering information

Part Number	Package	Delivery mode
PSM20N65CTF	ITO-220AB	50 pieces / tube

Note: For Halogen Free molding compound, add "H" suffix to part number above.

#### Mechanical

- Case: ITO-220AB
- Molder Plastic: UL Flammability Classification Rating 94V-0
- Device Weight : 0.06 ounces (1.74grams)
- Mounting Torque : 10 in-lbs maximum.

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